

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0021] with the following marked-up version of the paragraph:

[0021] In the description that follows, the invention is described with reference to acts and symbolic representations of operations that are performed by one or more computers, unless indicated otherwise. As such, it will be understood that such acts and operations, which are at times referred to as being computer-executed, include the manipulation by the processing unit of the computer of electrical signals representing data in a structured form. This manipulation transforms the data or maintains them at locations in the memory system of the computer, which reconfigures or otherwise alters the operation of the computer in a manner well understood by those skilled in the art. The data structures where data are maintained are physical locations of the memory that have particular properties defined by the format of the data. However, while the invention is being described in the foregoing context, it is not meant to be limiting as those of skill in the art will appreciate that several of the acts and operations described hereinafter may also be implemented in hardware. Figure 1 shows a schematic diagram of an example computer architecture usable for these devices. ~~be implemented in hardware.~~

Please replace paragraph [0055] with the following marked-up version of the paragraph:

[0055] Since invalid messages are not transmitted, network bandwidth and processing resources are preserved. In long running message exchange patterns, state information for the message exchange pattern may be loaded from persistent memory (i.e., loaded from durable state) to system memory, and saved from system memory to persistent memory (i.e., to durable state) in response to receiving messages. Accordingly, the transmission of invalid message may result in the transfer of large amounts of information between persistent memory and system memory even though the invalid message results in no progress. This is avoided since invalid message messages are never sent thereby resulting in considerable resource preservation for long running message exchange patterns.